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Needham Growth Conference

January 16, 2025

Safe Harbor

This presentation contains forward-looking statements concerning Atomera Incorporated (""Atomera," the "Company," "we," "us," and "our"). The words "believe," "may," "will," "potentially," "estimate," "continue," "anticipate," "intend," "could," "would," "project," "plan," "expect" and similar expressions that convey uncertainty of future events or outcomes are intended to identify forward-looking statements. These forward-looking statements are subject to a number of risks, uncertainties and assumptions, including those disclosed in the section "Risk Factors" included in our Annual Report on Form 10-K filed with the SEC on February 15, 2024 (the "Annual Report"). In light of these risks, uncertainties and assumptions, the forward-looking events and circumstances discussed in this presentation may not occur and actual results could differ materially and adversely from those anticipated or implied in our forward-looking statements. You should not rely upon forward-looking statements are reasonable, we cannot guarantee that the future results, levels of activity, performance or events and circumstances described in the forward-looking statements will be achieved or occur.

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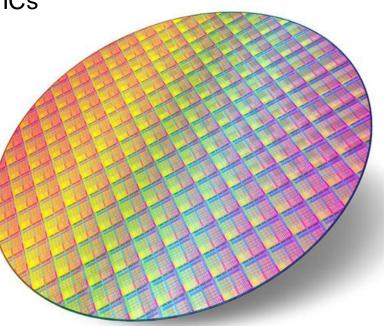
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Investment Overview

▶ Mears Silicon Technology (MST[®]) is a thin film used to enhance semiconductors

- Results in higher performance, lower power, and lower costs for ICs
- Capital-light IP and technology licensing business
- Engaged with 50% of world's top semiconductor makers
- Licenses with five companies including two JDAs
- Strong team to commercialize technology



MST: Mears Silicon Technology

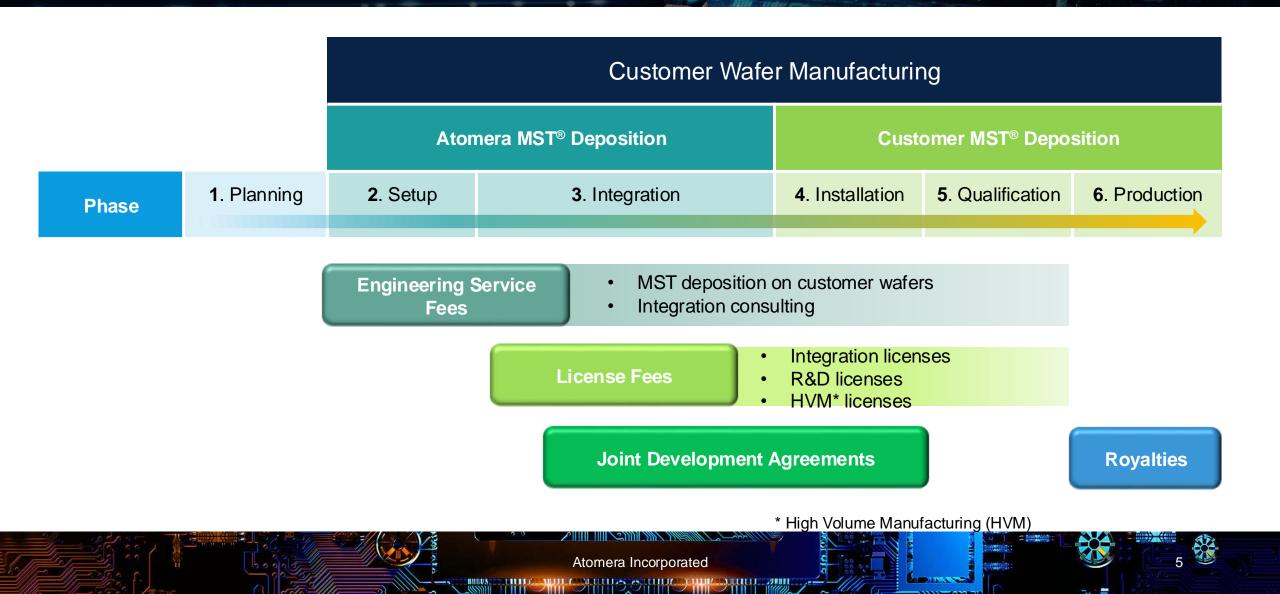
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Quantum Engineered Silicon



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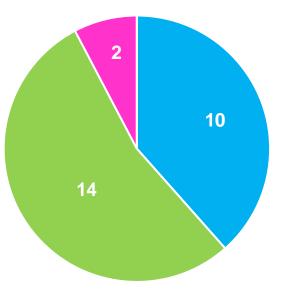
Customer Engagement & Revenue Model



Customer Status

			Customer Wafer Manufacturing				
		Atomera MST [®] Deposition		Customer MST [®] Deposition			
	Phase	1. Planning	2. Setup	3. Integration	4. Installation	5. Qualification	6. Production

Engagement Phases



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• 20 customers, 26 engagements

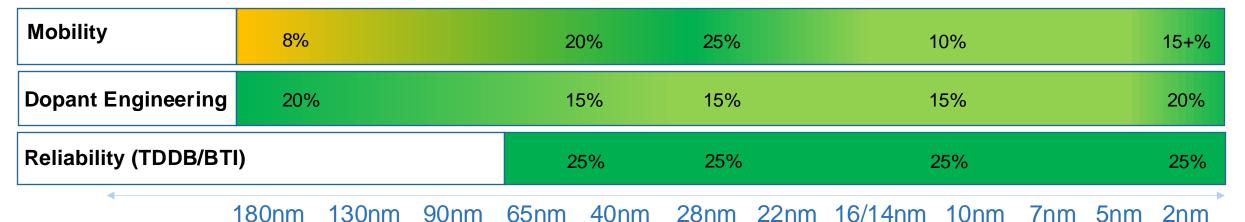
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 Working with more than half of the world's top semiconductor makers*

MST Key Benefits Across Nodes



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These Benefits are ADDITIVE & COMPLEMENTARY to other enhancement technologies

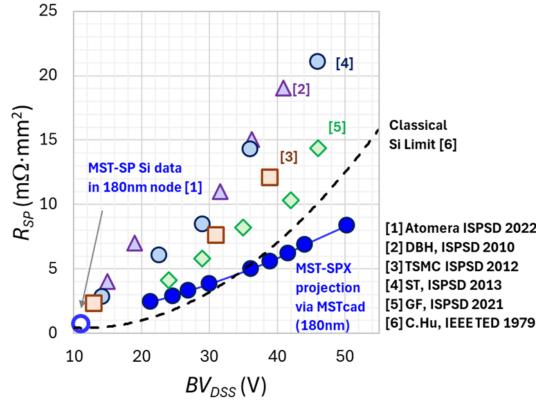


MST-SP, MST SPX for BCD technology focus areas **MST** for **RF-SOI MST** for Advanced MST for Nodes atomera DRAM

MST-SP/SPX for higher power BCD

- Analog & Power market represents \$52B in 2024
- Atomera introduced MST-SP for 5V in 2022
- In 2023 MST-SPX targeted 5-48V
 - Area of highest customer interest
- MST achieves best in class performance
 - MST-SPX beat all published results
 - Simulations predict up to 20% improvement
- ST Micro is one of the largest manufacturers

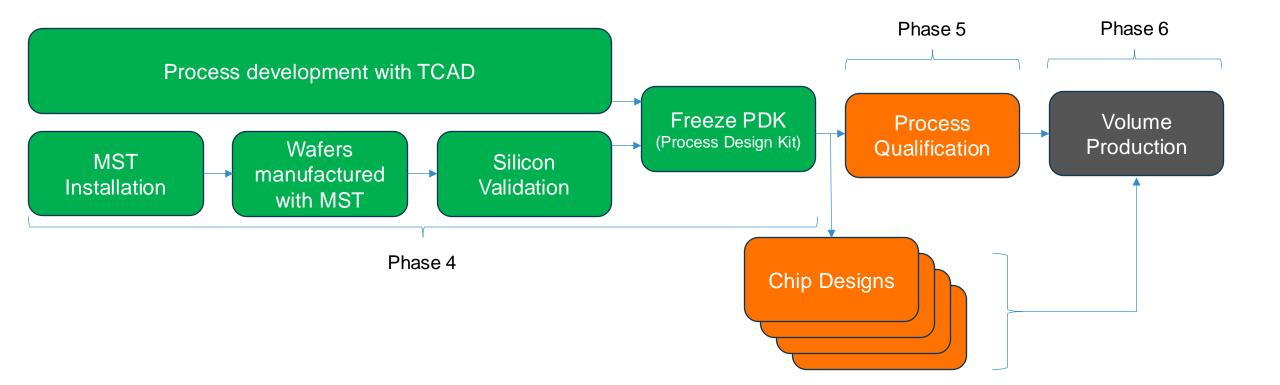
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Si power switch benchmarking

Typical Productization cycle

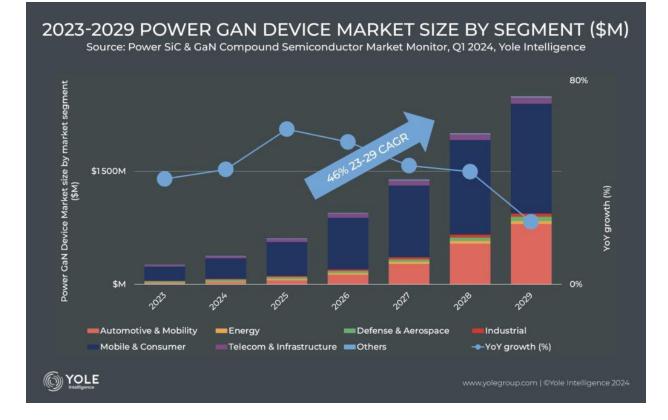






Compound Semiconductors

- Compound semiconductor market growing rapidly
- Poor wafer substrate quality causes manufacturing challenges
- Atomera's MST may help to solve this problem
- Experiments of GaN on MST show promising results
- Pushing to productize quickly



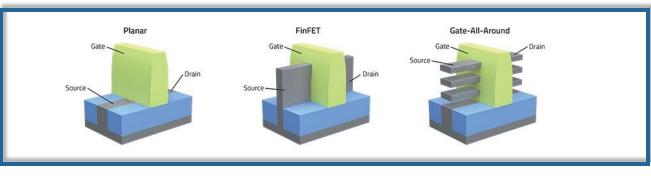
Advanced Node/GAA market



- GAA technology rapidly replacing FINFETs
 - In 3nm and below
- Snm alone projected to be \$26B by 2032
 - Driven by AI chip demand
- GAA structure requires extensive Epi
 - Low cost opportunity to add MST

MST: Solving GAA Transistor Challenges

- Blocks source/drain dopant diffusion
- Provides enhanced punch-through stop
- Lowers contact resistance
- Reduces HKMG stack height
- Improves carrier mobility, gate leakage



Transistor Architectural Evolution

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MST for DRAMs

DRAM market size is ~\$100B in 2024

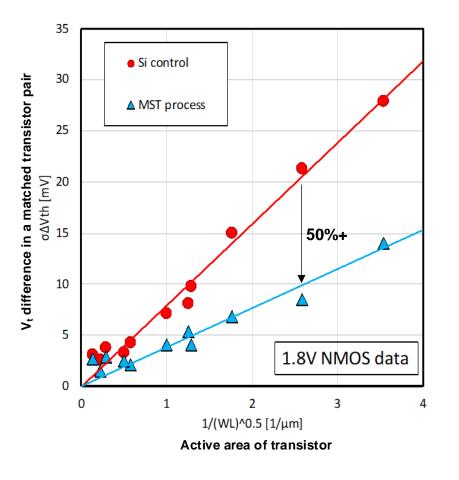
HBM is high growth segment focused on AI

High variability between transistors is a significant issue

This increases costs and limits the minimum achievable voltage and power

DRAM sense-amp variability is a major design constraint

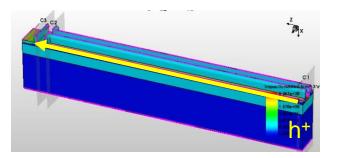
- Sense-amp margin defines refresh interval and resulting power
- Improving variability allows smaller sense-amp and reduced power
- MST can lower variability, critical in advanced nodes and memories

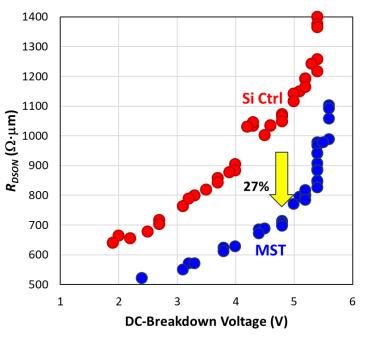


MST for **RFSOI** Devices

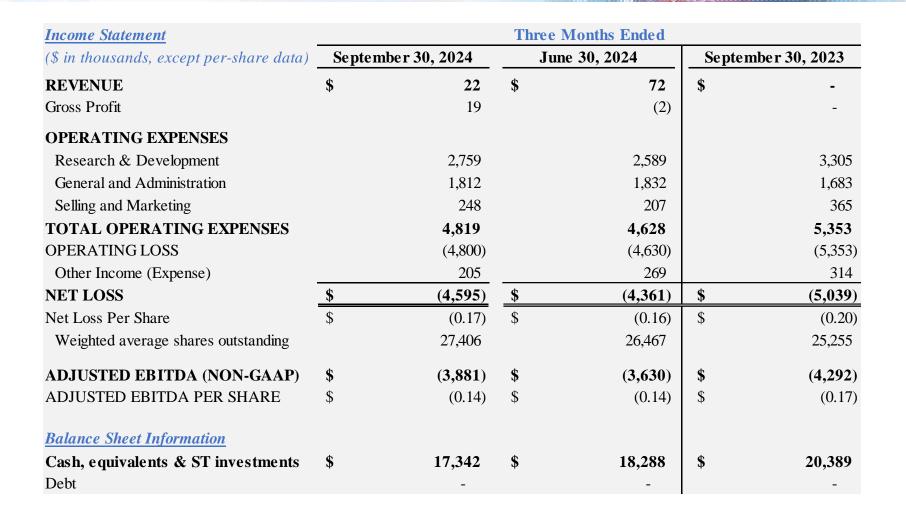


- RFSOI substrate market is ~\$600M in 2024
- Used in RF front-end of consumer devices
- Continued improvements in RF devices are critical to support new cellular standards
- MST is a rare tool to achieve those goals
- Atomera deeply penetrated
 - With RFSOI device manufacturers/designers
 - With RFSOI substrate suppliers





Financial Overview



Summary

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High margin, recurring revenue financial model

Strong technology, patent position, and balance sheet

Traction with many top industry players and growing licensee base

Ramping commercial license revenues

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Thank You

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